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Terms used **hop instruction**

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1 [Migratory applications](#)

Krishna A. Bharat, Luca Cardelli

December 1995 **Proceedings of the 8th annual ACM symposium on User interface and software technology**

Full text available:  [pdf\(1.19 MB\)](#)



Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: application checkpointing, application migration, collaborative work, interactive agents, mobile computing, safety, ubiquitous computing

2 [Performance monitoring: TEST: a tracer for extracting speculative threads](#)

Michael Chen, Kunle Olukotun

March 2003 **Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization**

Full text available:  [pdf\(1.76 MB\)](#)
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Thread-level speculation (TLS) allows sequential programs to be arbitrarily decomposed into threads that can be safely executed in parallel. A key challenge for TLS processors is choosing thread decompositions that speedup the program. Current techniques for identifying decompositions have practical limitations in real systems. Traditional parallelizing compilers do not work effectively on most integer programs, and software profiling slows down program execution too much for real-time analysis. ...

3 [Modulo scheduling: Modulo schedule buffers](#)

Matthew C. Merten, Wen-mei W. Hwu

December 2001 **Proceedings of the 34th annual ACM/IEEE international symposium on Microarchitecture**

Full text available:  [pdf\(1.38 MB\)](#)
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As VLIW/EPIC processors are increasingly used in real-time, signal-processing, and embedded applications, the importance of minimizing code size and reducing power is growing. This paper describes a new architectural mechanism, called the Modulo Schedule Buffers, that provides an elegant interface for the execution of modulo scheduled loops. While the performance is similar to that of kernel-only modulo scheduling, this mechanism has a number of advantages, including minimal code expansion. Rath ...

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"hop instruction" debug



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 ... 4-1-3. The **hop instruction** has been defined in the Animation editor window ... Of course, it also makes the program much easier to. **debug** (when things go wrong ...
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... A **hop instruction** is used by agents to move from one site. to the next. ...

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... The **hop instruction** can be programmed in Obliq as follows:. I.et hop =.

pmc (agentSerr ... One possible application of cloning is in **debug**- ...

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Architectures for mobile agents

... A **hop instruction** is used by agents to move from one site to the next. ...

there is a LALO itself and also some simple examples and some **debug** tools. ...

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... This requirement is also similar to source code **debug**- ... agent executes a

hop instruction, the suitcase and the com-. putation's closure are migrated ...

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... the Loop instruction shown below executes a **Hop instruction** (a named ...
to watch what went wrong in their programs and easily **debug** and correct them. ...

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[PS] user interface to interact with users. A major advantage of four ...

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... One possible application of cloning is in **debug**-ging. When a bug is encountered in

... If the **hop instruction** executes successfully, true is returned. ...

www.cc.gatech.edu/gvu/people/Phd/Krishna/Migration.ps.Z - [Similar pages](#)

package Darwin; // \$Id: DarwinConsts.java,v 1.34 1997/07/09 03:37 ...

... int NUMBER_OF_OPS = 10; /** The op code for the DULL **hop instruction** */ public final ...

in place of #ifdef blocks */ public static boolean **DEBUG** = true; /**@name ...

www.cs.duke.edu/~rodger/drool/pages/darwin/src/java/Darwin/DarwinConsts.java - 6k - Supplemental Result -
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